

Notice of References Cited	Application/Control No. 10/662,215	Applicant(s)/Patent Under Reexamination GORANTLA ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,891,205	04-1999	Picardi et al.	51/308
	C	US-6,620,215	09-2003	Li et al.	51/298
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	H	US-6,620,216	09-2003	Oshima et al.	51/309
	I	US-2003/0118824	06-2003	Tokarz et al.	428/404
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 313-14, 369, 382-84.
	V	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, p. 161.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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